

FIG. 1

10

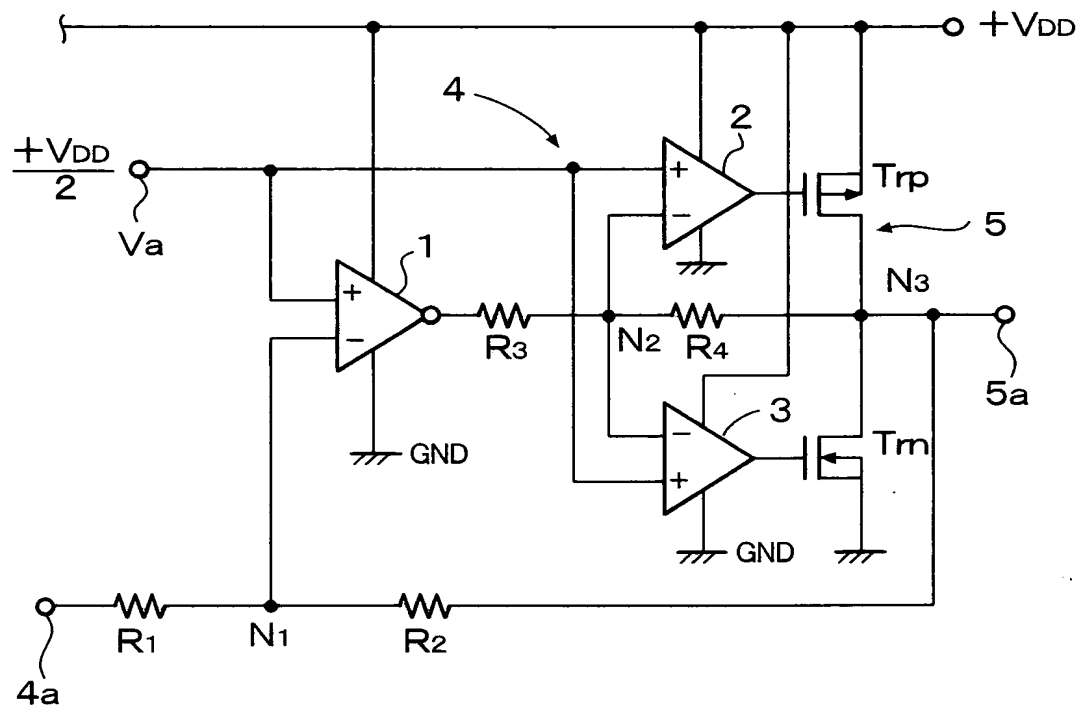
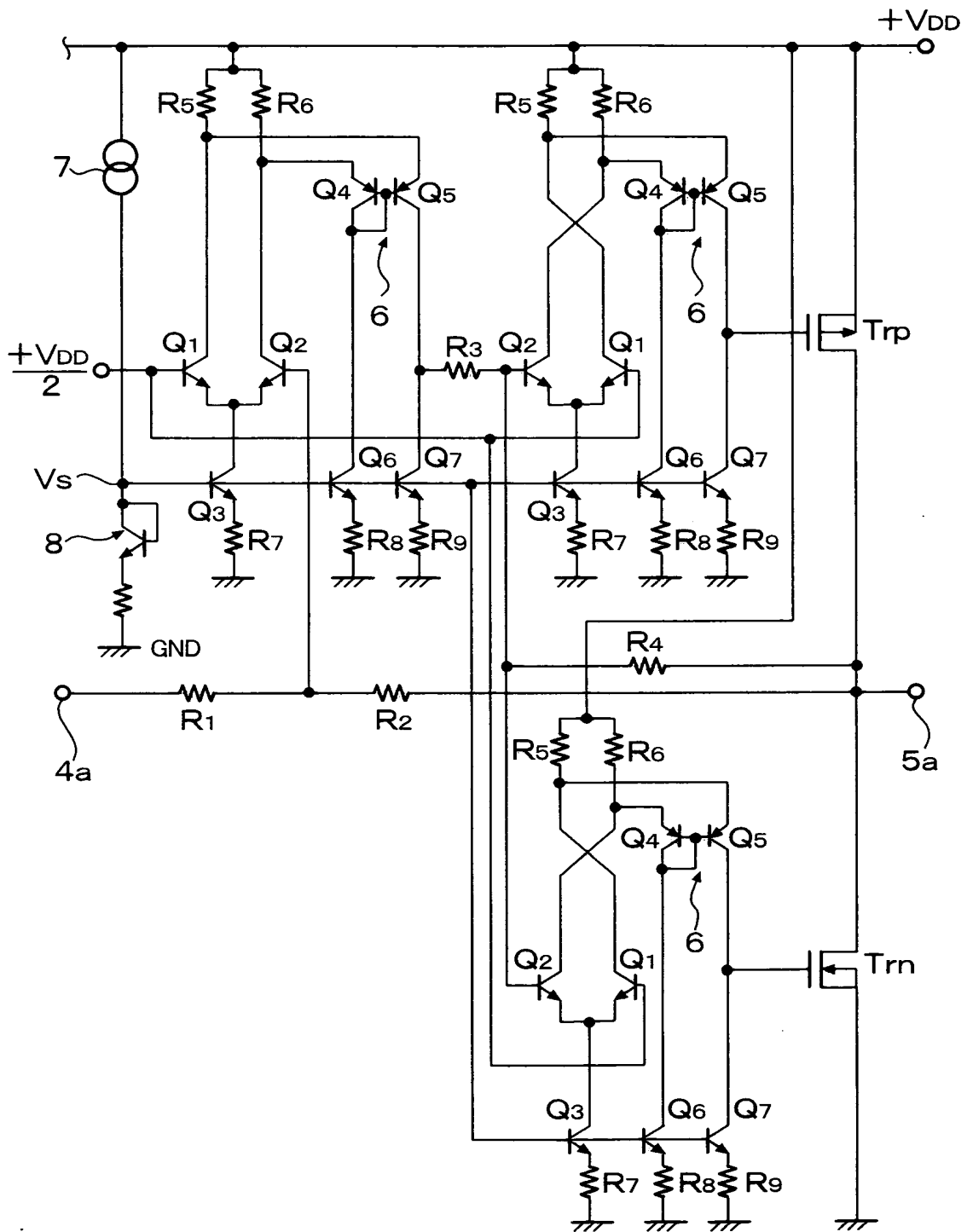


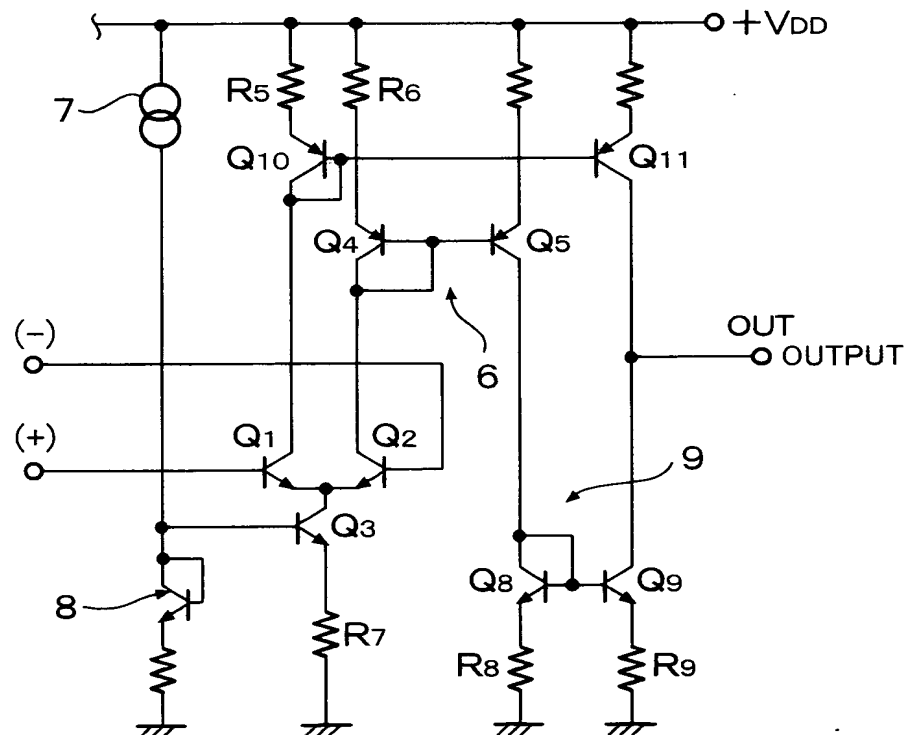
FIG. 2



• • • •



•
•
•
•



The circuit diagram shows an 11-transistor CMOS circuit. Key components and connections include:

- Power Supply:** $+V_{DD}$ at the top and **GND** at the bottom.
- Resistors:** R_5, R_6, R_7, R_8, R_9 are connected to various nodes and ground. A resistor is also connected to V_{DD} and the input node.
- Transistors:** $Q_1, Q_2, Q_3, Q_4, Q_5, Q_8, Q_9, Q_{10}, Q_{11}$ are labeled. Q_1 and Q_2 are PMOS devices, while the others are NMOS.
- Inputs:** A signal V_s is applied to the input node. A differential input pair is labeled with **(+)** and **(-)** terminals.
- Output:** The **OUTPUT** is taken from the node between Q_8 and Q_9 .
- Annotations:** Circled numbers 6, 7, 8, and 9 point to specific nodes or components in the circuit.